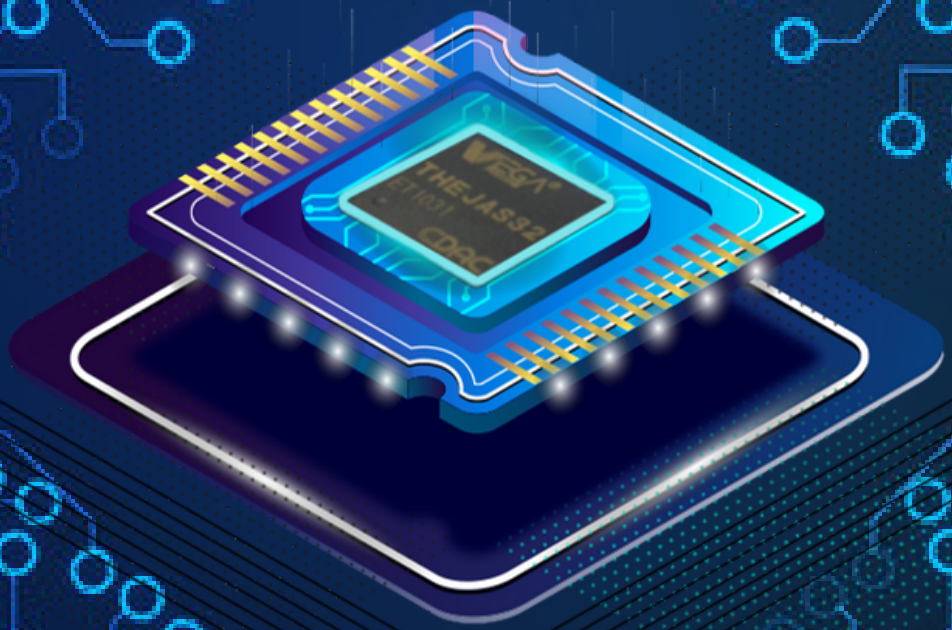
VEGA PROCESSOR

MEGA WORKSHOP ON DIGITAL INDIA RISC-V VEGA PROCESSORS

Unleashing the Power of VEGA: Igniting Innovation Across India

WWW.VEGAPROCESSORS.IN



Join us on a nationwide journey to explore the world of the VEGA Processor! Our mega workshop, spanning across various states in India, is your gateway to hands-on learning and innovation. Discover the limitless possibilities of VEGA and transform your ideas into reality. Get ready to embark on a transformative experience like never before!

9:00 AM-17:00 PM | NATIONWIDE | NOVEMBER 17-18, 2023

सी डैक CDAC

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MINISTRY OF ELECTRONICS AND INFORMATION TECHNOLOGY
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MEGA WORKSHOP ON DIR-V VEGA PROCESSORS

DIR-V VEGA PROCESSOR

VEGA Processors are a series of indigenous microprocessors developed by C-DAC under the Digital India RISC-V (DIR-V) program. IEEE India Council in association with C-DAC, MeitY is conducting a two day hands-on workshop on the VEGA processor ecosystem. This pan India workshop will be held at multiple venues parallelly.

ABOUT THE WORKSHOP

A national workshop on the newly launched indigenous RISC-V based DIR-V VEGA Processors jointly organised by C-DAC, MeitY along with the IEEE India Council. The training will be handled by experts from C-DAC, who are part of VEGA Processor development team. The DIR-V VEGA workshop will provide an insight into the VEGA series of processors and ecosystem focusing on both the theoretical and practical aspects including development boards, SDK and on building applications. The hands-on sessions will be conducted on the ARIES development board which is Arduino compatible. Target audience for this workshop is Students/Faculties/Research Scholars/Industry Professionals. Certificates will be issued after the successful completion of the workshop.

AGENDA

- Inaugural Session
- Technical sessions from industry experts
- Introduction to VEGA Microprocessors
- Overview of ARIES development board
- Sensor interfacing with ARIES board
- IoT application development

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REGIONAL VENUES

This pan India workshop will be held at the following venues parallelly.

- ALLIANCE UNIVERSITY, BENGALURU
- AMRITA UNIVERSITY, BENGALURU
- BHARATI VIDYAPEETH COLLEGE OF ENGINEERING, NEW DELHI
- BITS-PILANI, HYDERABAD
- CHANDIGARH UNIVERSITY, MOHALI
- COCHIN UNIVERSITY OF SCIENCE AND TECHNOLOGY, COCHIN
- GURU TEGH BAHADUR INSTITUTE OF TECHNOLOGY, NEW DELHI
- INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, BHOPAL
- KIET GROUP OF INSTITUTIONS, GHAZIABAD
- K L DEEMED TO BE UNIVERSITY, GUNTUR
- MODY UNIVERSITY OF SCIENCE AND TECHNOLOGY, SIKAR
- NETAJI SUBHASH ENGINEERING COLLEGE, KOLKATA
- PSG COLLEGE OF TECHNOLOGY, COIMBATORE
- THAKUR COLLEGE OF ENGINEERING AND TECHNOLOGY, MUMBAI
- VARDHAMAN COLLEGE OF ENGINEERING, HYDERABAD
- VELLORE INSTITUTE OF TECHNOLOGY, CHENNAI

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WHO CAN ATTEND?

The VEGA Processor Mega Workshop is open to a diverse range of participants, including students, research scholars, faculty members, and industry professionals who are eager to explore indigenous microprocessors and their capabilities.

- Student
- Research Scholar
- Faculty
- Industry professional

Limited seats are available in regional venues for the workshop, so please book your seats quickly to secure your spot

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REGISTRATION DEAILS

This pan India workshop will be held at multiple venues parallelly.

REGISTRATION LINK

Interested Students/Faculties/Research scholars/Industry Professionals can register in the below link.

https://forms.gle/Qb3QZE4XtB5X9ML97

[Last Date : Wednesday, 25.10.2023]

IMPORTANT DATES

Last Date for the Participants to Register for the Workshop:	25.10.2023
Announcement of the First List of Participants:	27.10.2023
Last date for Paying Registration Fee:	02.11.2023
Announcement of the Second List of Participants:	03.11.2023
Last date for Paying the Registration Fee:	10.11.2023
Mega VEGA Workshop at Regional Venues:	17.11.2023 - 18.11.2023
Certificate Distribution to Participants:	30.11.2023

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MEGA WORKSHOP ON DIR-V VEGA PROCESSORS

ORGANIZING TEAM

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INSTITUTIONAL COLLABORATORS

ALLIANCE UNIVERSITY
Private University established in Karnataka State by Act No.34 of year 2010
Recognized by the University Grants Commission (UGC), New Delhi

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